

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD OF FORMING A LOW VOLTAGE GATE OXIDE LAYER AND TUNNEL OXIDE LAYER IN AN EEPROM CELL	
Application Number :	10/717149	
Date :	2003-11-18	
First Named Applicant:	Renninger Alan L.	
Confirmation Number:	6780	
Attorney Docket Number:	ATM-262	

I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Submitted By:	Elec. Sign.	Sign. Capacity
Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney

Documents being submitted:	Files
us-ids	Atm262-usidst.xml us-ids.dtd us-ids.xsl
Comments	